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APPLICATION NO. 09/745919	CONT/PRIOR	CLASS 370	SUBCLASS 220	ART UNIT 2661	EXAMINER K. Dang
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Thomas Bayerl  
Christopher Tumas

TITLE	APPLICANTS
1. <b>Chairman</b>	1. <b>Mr. J. Edgar Hoover</b>
2. <b>Vice Chairman</b>	2. <b>Mr. J. Edgar Hoover</b>
3. <b>Secretary</b>	3. <b>Mr. J. Edgar Hoover</b>
4. <b>Member</b>	4. <b>Mr. J. Edgar Hoover</b>
5. <b>Member</b>	5. <b>Mr. J. Edgar Hoover</b>
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97. <b>Member</b>	97. <b>Mr. J. Edgar Hoover</b>

Use of bus hold to prevent bus contention between high speed processor and slow peripheral

P.T.O.-2040  
12/89

<b>ISSUING CLASSIFICATION</b>									
<b>ORIGINAL</b>				<b>CROSS REFERENCE(S)</b>					
<b>CLASS</b>		<b>SUBCLASS</b>		<b>CLASS</b>	<b>SUBCLASS (ONE SUBCLASS PER BLOCK)</b>				
<b>INTERNATIONAL CLASSIFICATION</b>									

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	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) _____ (Date)			<b>NOTICE OF ALLOWANCE MAILED</b>  	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent. No. _____ _____	_____ (Primary Examiner) _____ (Date)				
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner) _____ (Date)			<b>ISSUE FEE</b> Amount Due _____ Date Paid _____	
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